

This open access document is posted as a preprint in the Beilstein Archives at https://doi.org/10.3762/bxiv.2025.49.v1 and is considered to be an early communication for feedback before peer review. Before citing this document, please check if a final, peer-reviewed version has been published.

This document is not formatted, has not undergone copyediting or typesetting, and may contain errors, unsubstantiated scientific claims or preliminary data.

Preprint Title Programmable Soliton Dynamics in All-Josephson-Junction Logic

Cells and Networks

Authors Vsevolod I. Ruzhickiy, Anastasia A. Maksimovskaya, Sergey V.

Bakurskiy, Andrey E. Schegolev, Maxim V. Tereshonok, Mikhail Y.

Kupriyanov, Nikolay V. Klenov and Igor I. Soloviev

Publication Date 01 Aug. 2025

Article Type Full Research Paper

ORCID® iDs Vsevolod I. Ruzhickiy - https://orcid.org/0000-0002-3411-7050;

Anastasia A. Maksimovskaya -

https://orcid.org/0009-0002-4197-2263; Andrey E. Schegolev - https://orcid.org/0000-0002-5381-3297; Maxim V. Tereshonok - https://orcid.org/0000-0003-1330-281X; Mikhail Y. Kupriyanov -

https://orcid.org/0000-0003-1204-9664



Programmable Soliton Dynamics in All-Josephson-Junction Logic

Cells and Networks

- ³ Vsevolod I. Ruzhickiy^{1,2}, Anastasia A. Maksimovskaya*^{1,2,3}, Sergey V. Bakurskiy^{1,4}, Andrey E.
- ⁴ Schegolev¹, Maxim V. Tereshonok⁵, Mikhail Yu. Kupriyanov¹, Nikolay V. Klenov³ and Igor I.
- 5 Soloviev^{1,2,4}
- ⁶ Address: ¹Lomonosov Moscow State University, Skobeltsyn Institute of Nuclear Physics, Moscow,
- ⁷ 119991, Russia; ²All-Russian Research Institute of Automatics n.a. N.L. Dukhov (VNIIA),
- 8 127055, Moscow, Russia; ³Lomonosov Moscow State University, Faculty of Physics, Moscow,
- ₉ 119991, Russia; ⁴Moscow Institute of Physics and Technology, 141700 Dolgoprudny, Russia and
- ⁵Moscow Technical University of Communications and Informatics (MTUCI), 111024, Moscow,
- 11 Russia
- Email: Anastasia A. Maksimovskaya stasyahime@gmail.com
- * Corresponding author

14 Abstract

- We demonstrate programmable control of kinetic soliton dynamics in all-Josephson-junction (all-
- 16 JJ) networks through a novel tunable cell design. This cell enables on-demand switching of trans-
- mission lines and operates across defined parameter regimes supporting diverse dynamical modes.
- By introducing a structural asymmetry into a transmission line, we implement a Josephson diode
- that enforces unidirectional soliton propagation. The programmability of the kinetic inductance
- 20 then provides a crucial mechanism to selectively enable or disable this diode functionality. By
- engineering artificial inhomogeneity into the circuit architecture, we enhance robustness in all-JJ
- logic circuits, 2D transmission line all-JJ lattices, and neuromorphic computing systems.

23 Keywords

- ²⁴ superconducting electronics; superconducting neural networks; kinetic inductance; soliton dynam-
- 25 ics; Josephson-based diode

26 Introduction

- ²⁷ The rapid advancement of Josephson junction (JJ) logic circuits [1-5] and neuromorphic networks
- ²⁸ [6-9] holds transformative potential for ultra-low-power computing. However, achieving scalable
- 29 integration remains a critical bottleneck, as conventional JJ-based architectures face fundamental
- 30 density constraints imposed by magnetic flux manipulation requirements and complex mutual in-
- 31 ductive crosstalks.
- ³² Circuits composed entirely of Josephson junctions (all-JJ circuits) [10-16] represent a promising
- platform for energy-efficient, high-speed and scalable computing. In these systems, the propaga-
- tion of information is associated with the movement of a current wave / topological soliton, which
- is clearly visible in the model by a 2π -jump of the so-called Josephson phase, φ . In contrast to con-
- ventional Rapid Single Flux Quantum (RSFQ) logic, the phase drop for the considered Single Ki-
- netic Soliton (SKS) occurs not on the relatively large connecting geometric inductors, but on the
- Josephson junctions. SKS is a propagating wave of phase change with limited from below kinetic
- energy, the corresponding current pulse "dissipates" if its motion is interrupted, for example, by a
- 40 structural inhomogeneity in a transmission line. Traditionally, this sensitivity to structural inhomo-
- geneities has been viewed as a challenge for robust circuit design.
- In this work, we propose to exploit the sensitivity mentioned above. We base our proposal on the
- concept of applying a small number of key cells, which should create precisely engineered tun-
- 44 able inhomogeneities. Such inhomogeneity may be designed as an element of tunable kinetic
- inductance [17]. This element has high inductance at small scales and can be controlled using
- currents [18,19], voltage [20] or magnetic fields [21,22]. At the same time, the use of hybrid
- superconductor-normal metal structures makes it possible to increase the effect of frequency tun-
- ing [23,24], while the addition of ferromagnetic layers permits the non-volatile control [25,26].

The another feature of tunable kinetic inductance element is the linear behaviour for weak signals, which excludes formation of parasitic processes in the transmission line. This permits to apply tunable kinetic inductance in the resonators with shifting resonance frequency [19,21,22], as well as in sensitive all-JJ digital circuits. This idea enables us to use the 'flaws' of the structure as its important features, opening up a pathway to creating programmable and reconfigurable large circuits. An obvious and widely required application of this technology is in the development of superconductive programmable gate arrays 55 (SPGA) [27-30], an active area of current research. Another important application of this idea lies 56 in the promising neuromorphic direction [31-33]. Earlier in [34], we have already proposed using 57 kinetic inductances to control neuron dynamics in networks based on radial basis functions (RBFnetworks). Moreover, this approach can be extended to hardware realisations of bio-inspired spiking neural networks [35-42] by solving the challenges of creating controllable synapses to realize 60 the effect of spike-timing-dependent plasticity and unidirectional feedbacks for self-regulation. Fur-61 thermore, the physical resemblance between solitons and the action potentials (spikes of voltage) 62 generated in biological nervous systems makes all-JJ structures tempting candidates for constructing neuromorphic hardware [43]. 64 In this paper, we investigate the use of controlled kinetic inductance to create an engineered inhomogeneous medium for kinetic solitons. We demonstrate that by tuning this inhomogeneity, distinct dynamical modes can be induced, fundamentally altering the soliton's behavior. Furthermore, we explore how structural asymmetry within this medium can be exploited to achieve a diode ef-68 fect, enabling non-reciprocal soliton propagation. Building upon these foundational concepts, we then propose two specific architectural solutions: a programmable switch and a versatile routing matrix, which we term the "WayMatrix". We suggest that these architectures provide a framework for the flexible configuration of advanced logic and neuromorphic circuits.

3 Results

78

4 Model description

To model the dynamics of kinetic solitons [43], we employ the Resistively and Capacitively
Shunted Junction (RCSJ) model [1], where the total current *I* across a Josephson junction is the
sum of the supercurrent, the quasi-particle current, and the displacement current:

$$I = I_c \sin(\varphi) + \frac{V}{R_N} + C\frac{dV}{dt}.$$
 (1)

Here, φ is the phase difference for the complex superconducting order parameter across the junction, V is the voltage, I_c is the critical current, R_N is the resistance in normal state and C is the capacitance. For analysis, it is convenient to express this equation in a dimensionless form. We normalize the time to the inverse of a reference plasma frequency, $\tau = \tilde{\omega}_p t$, where $\tilde{\omega}_p = \sqrt{2\pi \tilde{I}_c/(\Phi_0 \tilde{C})}$, and normalise the current to a reference critical current \tilde{I}_c . This yields:

$$i = A \cdot \sin(\varphi) + \alpha \dot{\varphi} + \ddot{\varphi}. \tag{2}$$

In this normalized equation, the dots above the phases indicate differentiation over time with respect to τ . The dimensionless damping coefficient is $\alpha = \Phi_0 \tilde{\omega}_p/(2\pi \tilde{I}_c R_N)$. The term $\dot{\varphi}$ represents the voltage normalized by the characteristic voltage $V_0 = \Phi_0 \tilde{\omega}_p/(2\pi)$. The parameter $A = I_c/\tilde{I}_c$ is the normalized amplitude of the critical current for junctions with the critical current I_c that differs from the reference normalization value \tilde{I}_c .

To analyze the circuit dynamics, we adopt a nodal analysis approach. In this approach, the gauge-invariant phase difference across any element is expressed in terms of the nodal phases at its terminals, $\varphi = \varphi_k - \varphi_j$. The phase of the ground node is set to zero by convention. This formulation inherently satisfies Kirchhoff's Current Law (KCL) at each node. For any node k connected to H

elements, KCL dictates that the algebraic sum of currents is zero:

99

101

$$\sum_{h=1}^{H} I_{k,j(h)} = 0, (3)$$

where the index h runs over all elements connected to node k, j(h) is the index of the node at the other end of element h, and $I_{k,j(h)}$ is the normalised current flowing from the node k to the node j(h). Each current is described by the RCSJ model (Eq. 2):

$$I_{k,i(h)}/\tilde{I}_c = A_h \sin(\varphi_k - \varphi_{i(h)}) + \alpha_h(\dot{\varphi}_k - \dot{\varphi}_{i(h)}) + (\ddot{\varphi}_k - \ddot{\varphi}_{i(h)}). \tag{4}$$

With this approach, the current across the inductance is defined by the expression

$$I_{k,j(h)}^{L}/\tilde{I}_{c} = \frac{\Phi_{0}}{2\pi L\tilde{I}_{c}}(\varphi_{k} - \varphi_{j(h)}) = (\varphi_{k} - \varphi_{j(h)})/l, \tag{5}$$

where $l = L/L_J$ is inductance normalised to the Josephson inductance $L_J = \Phi_0/(2\pi \tilde{I}_c)$.

After substituting the expressions for the current into the formula for the current balance at the node, we get:

$$M_{k,k}\ddot{\varphi}_k - \sum_{h=1}^H M_{k,j(h)}\ddot{\varphi}_{k,j(h)} = F_k(\varphi_k, \varphi_{k,j(h)}, \dot{\varphi}_k, \dot{\varphi}_{k,j(h)}), \tag{6}$$

where $M_{k,k}$ is the sum of the coefficients before $\ddot{\varphi}_k$, $M_{k,j(h)}$ are the coefficients before $\ddot{\varphi}_{k,j(h)}$, F_k contains the sum of all summands except those that do not contain the second derivative. In F_k , all summands with φ_k are written with a minus sign, and all summands with $\varphi_{k,j(h)}$ are written with a plus sign. Additional currents (e.g. the bias current or the time-dependent current from the generator) are also included as components. After writing down the equations 6 for each node, a system of second-order diffeomorphic equations are obtained, which can be represented in matrix

112 form:

$$\hat{M}\varphi = \begin{pmatrix} M_{11} & M_{12} & \dots & M_{1N} \\ M_{21} & M_{22} & \dots & M_{2N} \\ \vdots & \vdots & \ddots & \vdots \\ M_{N1} & M_{N2} & \dots & M_{NN} \end{pmatrix} \begin{pmatrix} \ddot{\varphi}_1 \\ \ddot{\varphi}_2 \\ \vdots \\ \ddot{\varphi}_N \end{pmatrix} = \mathbf{F}(\varphi, \varphi). \tag{7}$$

The resulting system of N ordinary differential equations is expressed in the matrix form shown in Eq. 7. In this equation, $\vec{\varphi}$ is the vector of nodal phases, N is the total number of non-ground nodes, and \hat{M} is the $N \times N$ mass matrix (also known as the capacitance matrix), which is defined by the capacitive coupling coefficients from Eq. 6. A key property of \hat{M} is its sparsity, which arises directly from the local connectivity of the circuit topology; each node is connected to a small subset of other nodes. To increase computational efficiency, we exploit this sparsity when solving the system. The equations are integrated numerically using an adaptive-step-size solver based on the explicit Runge-Kutta (4^{th} and 5^{th} order) formula, commonly known as the Dormand-Prince pair [44,45], which is well-suited for this class of non-stiff problems.

The Kinetic Inductance Controllable Key

The fundamental building block of our design is the Kinetic Inductance Controllable Key (KICK), 124 which is constructed from the two modified unit cells of an all-Josephson Junction Transmission 125 Line (all-JJTL). As depicted in Figure 1a, each cell is modified by incorporating a controlled ki-126 netic inductance in series with one of its Josephson junctions connected to the ground plane. There are some operational regimes inherent to such KICK governed by the value of this inductance and 128 by the damping parameter of junctions within the transmission line. The damping parameter is a critical factor as it dictates the kinetic soliton's propagation rate. 130 As a preliminary step, we characterized the dependence of the kinetic soliton propagation velocity 131 on the damping parameter of the connecting junctions, α (see 1b). We define the velocity as the 132 number of grounded junctions traversed per unit of normalized time, τ . Our simulations revealed

a critical damping threshold at $\alpha_{crit} \approx 0.8$; below this value stable soliton propagation is not sup-134 ported. Besides, under this condition the energy dissipation rate is too high relative to the energy 135 transfer between adjacent junctions, causing the soliton to decay. In case when $\alpha > \alpha_{crit}$, the soli-136 ton velocity is a monotonically increasing function of the damping. This dependence falls into an 137 approximately linear regime for $\alpha > 3$. The physical mechanism for this velocity increase can be 138 understood from the RCSJ model: a higher value of α enhances the resistive quasiparticle current 139 $(\alpha \dot{\varphi})$ that flows as a junction switches. This larger current provides a stronger driving force to the 140 next junction in the line, causing it to reach its critical threshold and switch more rapidly, thus in-141 creasing the overall propagation velocity of the soliton. 142

- The functionality of the KICK is determined by the interplay between the damping α and the normalized kinetic inductance L/L_J . Figure 1c summarizes the behavior of the device in a parameter map, which reveals four distinct operational regimes.
- Open Mode: The KICK is effectively transparent, allowing an incident kinetic soliton to propagate through it with minimal perturbation.

148

152

153

- Close Mode: The KICK acts as a terminator, blocking and destroying the incoming soliton.
- T-Mode: The KICK functions as a T-flip-flop. It possesses two stable states, and each arriving soliton toggles the cell from its current state to the other. Every second soliton passes to the exit.
 - M-Mode (Multy-State Mode): This regime is characterized by the formation of more than two stable states and other complex dynamics, which fall outside the scope of this study.
- An essential feature of the KICK is the ability to switch between different modes at a fixed alpha value: thus, by fixing alpha (for example, $\alpha = 2$) and varying the kinetic inductance, we can switch between all modes (**Open Mode** \rightarrow **Close Mode** \rightarrow **Open Mode** \rightarrow **T-Mode** \rightarrow **M-Mode**) represented on the parameter map (see Figure 1).
- To illustrate the operational modes of the KICK, we simulated the propagation of a kinetic soliton through the all-JJTL. The simulated line comprises 31 grounded junctions with a uniform damping

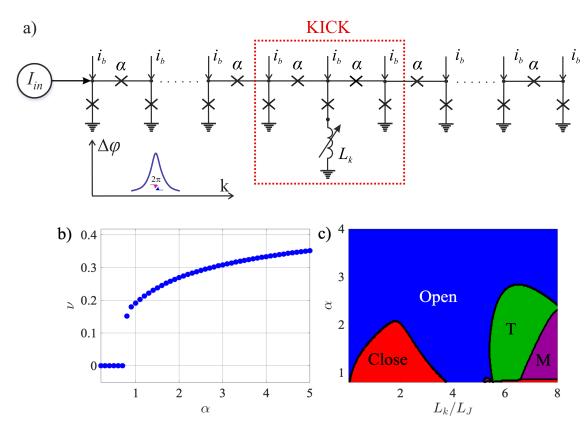


Figure 1: a) An equivalent scheme for the Kinetic Inductance Controllable Key (KICK) as a part of an all-Josephson transmission line. A soliton which dynamics is controlled by the key developed is represented schematically. **b)** Dependence of the single kinetic soliton (SKS) propagation velocity, measured in Josephson junctions per normalized time unit, on the damping parameter α ; **c)** Map of different modes depending on the damping parameter and kinetic inductance measurement. Close mode (red zone): the KICK does not allow SKS to pass through. Open mode (blue zone): the KICK passes SKS. T-mode (green zone): the KICK has two stable states and every second SKS passes through it. M-mode (purple zone): the KICK has many stable states.

parameter of connecting junctions $\alpha = 1$. The KICK is implemented by inserting a controlled 160 kinetic inductance in series with the ground junction at the line's center (node k = 16). Figure 161 2 presents the results for different values of this inductance, corresponding to distinct operational 162 modes. Each panel displays two key physical quantities on dual y-axes: 163 1) The spatial profile of the nodal Josephson phases (φ_k) as a function of the node index k. 164 2) The normalized currents flowing through the series junctions connecting the nodes. The current 165 between nodes k and k + 1 $(I_c/\tilde{I}_c sin(\varphi_{k+1} - \varphi_k))$ is plotted at the midpoint index k + 0.5 for visual 166 clarity. This visualization allows for a direct comparison of the system's state before and after soli-167 ton interaction. The solid lines depict the initial state (before the soliton reaches the KICK), and the 168 dashed lines show the final state (after the soliton has passed and the system has settled). 169 For a low inductance of $L/L_J=0.1$ (see Figure 2a), corresponding to the Open Mode, the KICK 170 causes only slight disturbance in the transmission line. The incident soliton propagates through 171 it unimpeded, and the entire line returns to its initial physical state. However, increasing the in-172 ductance to $L/L_J = 2$ (see Figure 2b) switches the system to the Close Mode. In this mode, the 173 KICK serve as a significant barrier; when the soliton arrives, the large inductance impedes the nec-174 essary current dynamics, halting the propagation and causing the soliton to be annihilated. Conse-175 quently, the 2π phase slip, which signifies the soliton's passage, only traverses the first half of the 176 line (nodes 1 to 15), while the segment beyond the KICK remains entirely unperturbed. Remark-177 ably, a further increase of inductance to $L/L_J=4$ (see Figure 2c) leads to the re-emergence of the 178 Open Mode. This non-trivial effect is governed by transient energy storage in the inductor L. Al-179 though the soliton is momentarily halted at the KICK, the subsequent release of stored magnetic energy provides the necessary "kick" to complete the phase slip at node 16. This re-initiates the 181 propagation, allowing the soliton to effectively re-form and travel down the rest of the line. Sim-182 ilarly to the low inductance case, the soliton successfully traverses the entire line, and the system 183 returns to its initial physical state. 184 The behavior of the KICK in the T-Mode, which enables its use as a T-flip-flop, is detailed in Fig-185 ure 2d. This mode is defined by the existence of two distinct stable states, physically correspond-

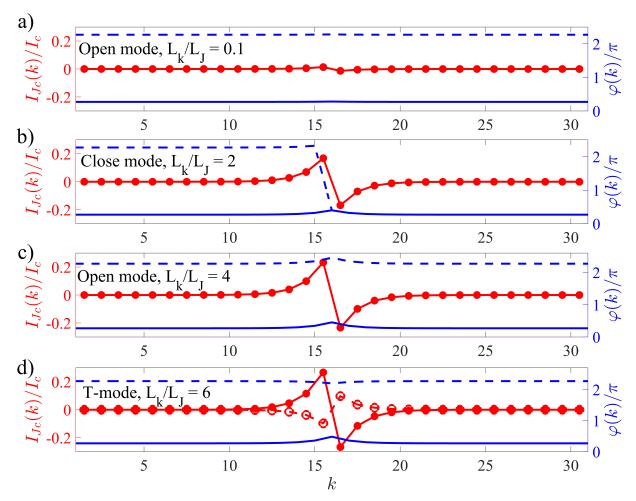


Figure 2: Spatial distributions of the Josephson phase (blue curves) and current (red curves) in: (a,c) open mode, $L_k/L_J = 0.1$ (a) and $L_k/L_J = 4$ (c); (b) close mode, $L_k/L_J = 2$; and (d) T-mode, $L_k/L_J = 6$. Solid lines show initial profiles, dashed lines represent distributions after soliton passage. The Josephson phase is plotted against the integer node index k, whereas the current is plotted at the midpoint index k + 0.5 to represent the junction between nodes k and k + 1, see Figure 1a.

ing to a bistable potential landscape created by the KICK architecture. These two states are distin-187 guished by the presence of persistent, static currents of opposite polarity flowing from the central 188 node (k = 16). This physical difference leads to an fundamentally state-dependent and asymmetric 189 toggling action. When the KICK is in the first stable state, an incoming soliton successfully flips it 190 to the second state and is transmitted, continuing its propagation down the line. Conversely, when 191 starting from the second state, an arriving soliton again flips the KICK back to the first state, but is 192 annihilated in the process and does not propagate further. This state-dependent transmission and 193 annihilation is the core mechanism that allows the KICK to function as a memory element or a dy-194 namic routing switch. 195 Beyond primary operational modes, the system exhibits other notable behavior types in specific 196 regions of its parameter space. The M-Mode, for instance, is characterized by complex responses, 197 depend on previous events. This can include such behavior when an initial soliton is annihilated, 198 effectively "priming" the cell to transmit all subsequent solitons, a feature potentially useful for 199 tasks like sequential filtering. Furthermore, in the transition regions between the primary modes, 200 we observe phenomena such as soliton reflection back towards the source. 201 Finally, the asymptotic behavior in the high-damping (α) limit is particularly significant. As α in-202 creases, so does the soliton's velocity and kinetic energy. Consequently, for sufficiently high α , the 203 soliton's energy is large enough to overcome any potential barrier presented by the KICK, ensur-204 ing transmission regardless of the inductance value. This results in a universal Open Mode at high rates. Crucially, this high-energy passage is not inert; if the KICK is in a bistable regime (such as 206 the T-Mode), the "passing" soliton can still deliver enough of an impulse to toggle the cell's state. 207

The Soliton Diode

What is even more interesting is that the KICK architecture can be engineered to function as a soliton diode, a device the function of which is similar to that of a semiconductor diode, allowing the soliton to pass in only one direction. This is achieved by introducing a structural asymmetry into the cell's design. It is important to note that such non-reciprocal behavior can be achieved even

```
without the kinetic inductance (L = 0). However, the inclusion of one (a tunable inductance) is a
    key innovation, as it allows to dynamically switch this directional property on and off.
    We demonstrate this principle through simulation of a KICK with L/L_J = 2. In our model, the
215
    transmission line's series junctions have a nominal critical current of I_c = 0.7\tilde{I}_c. The asymmetry
216
    is created by increasing the critical current of the specific junction connecting nodes 15 and 16 to
217
    I_c = \tilde{I}_c (i.e., to 1 in normalized units). The effect of this asymmetric potential barrier is that a
218
    soliton initiated in the forward direction (from node 1) successfully overcomes it and is transmitted
219
    along the entire line. In contrast, a soliton propagating in the reverse direction (from node 31) is
220
    unable to pass the barrier and is annihilated at node 17.
221
    Figure 3 demonstrates the non-reciprocal behavior of the soliton diode by showing a sequence
222
    of five snapshots of the nodal Josephson phase distribution at successive moments in time, ar-
223
    ranged from top to bottom. The process begins with the line in its initial state (top panel), after
224
    which a soliton is initiated from the left side (node 1). As shown in the second panel, this forward-
225
    propagating soliton successfully passes through the diode, resulting in a 2\pi phase advance across
226
    all nodes. Immediately after, a new soliton is initiated from the right side (node 31) to test the
227
    reverse direction. The third panel reveals that this soliton is blocked; its propagation is halted at
228
    the diode, and the corresponding 2\pi phase slip is confined to nodes 17 through 31. The fourth
229
    panel confirms the robustness of this blocking action, as a second, subsequent reverse-propagating
230
    soliton is also annihilated in the same manner. To complete the demonstration, another forward-
231
    propagating soliton is sent from the left. The fifth panel confirms that the diode once again allows
232
    it to pass, resulting in another full 2\pi phase advance across the entire line. It is crucial to note that
233
    although the absolute phase values accumulate in multiples of 2\pi throughout this sequence, the
234
    physical state of the structure remains unchanged after each full transmission, a direct consequence
235
    of the 2\pi periodicity of the Josephson energy.
236
    A significant feature of this structure is the ability to disable the diode effect. By increasing the
237
    inductance to L/L_J = 3, the device becomes bi-directionally transparent, effectively turning the
238
    diode function "off". This demonstrates how the introduced structural asymmetry alters the oper-
```

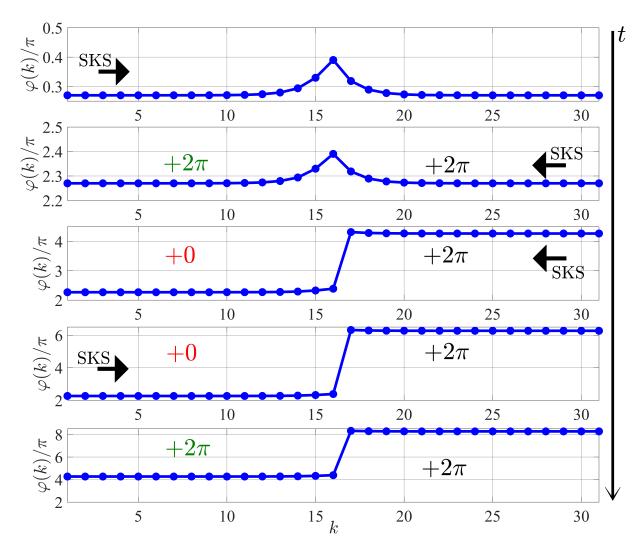


Figure 3: Temporal evolution of Josephson phase asymmetry in a soliton diode: (a) Initial state; (b) After left-propagating soliton passage; (c,d) Sequential right-propagating soliton interactions; (e) Final left-propagating soliton recurrence.

ational landscape of the device: an inductance value that would normally correspond to the Close Mode in a symmetric KICK now matches to a bi-directional Open Mode for the asymmetric diode structure. Furthermore, it is worth noting that the asymmetry required for diode-like behavior 242 can be achieved through alternative means, such as by creating a local mismatch in the damping 243 parameter — for instance, by increasing α from 1 to 3 for one of the series junctions instead of the 244 critical current. The ability to enforce a specific direction of soliton flow makes the soliton diode an essential com-246 ponent for complex circuit design. This is particularly critical in architectures involving feedback 247 loops, where it is necessary to unambiguously define the direction of signal propagation. This con-248 cept can be extended by cascading two such tunable diodes with opposing forward directions. This 249 configuration creates a programmable transmission line where the permitted direction of soliton 250 travel can be pre-configured by setting the inductance values of each diode. 251

Discussion

Implementation of reconfigurable networks

On the basis of the operational principles of the Kinetic Inductance Controllable Key and the soliton diode, we now demonstrate how these fundamental building blocks can be integrated to create 255 reconfigurable soliton-based logic circuits. We begin by proposing a specific proof-of-concept design for a signal routing network and then introduce a generalized, scalable architecture suitable for 257 complex computational tasks. 258 As a direct application of the KICK's switching capabilities, we first propose the 3-input, 3-output 259 routing network illustrated in Figure 3a. The proposed architecture is based on a grid where each 260 path depicted is itself a complete all-Josephson-junction transmission line (all-JJTL). The routing 261 mechanism would depend on the incorporation of KICKs into specific segments of these all-JJTLs. 262 By programming each KICK to be in either its Open Mode (transmitting) or Close Mode (block-263 ing), one could control the flow of solitons through the network and define a unique path from 264 any input to any output. To prevent collisions between solitons traveling along different routes, 265

the design incorporates auxiliary buffer lines. These lines make it possible to define a set of non-266 intersecting paths for all required connections, thus ensuring collision-free operation. This design 267 serves to validate the fundamental principle of using KICKs as programmable switches. 268 With this idea, we propose a more general and powerful architecture, which we term the "Way-269 Matrix", shown schematically in Figure 3b. This versatile NxM routing matrix is conceived as a 270 core component of larger soliton-based processors. Its enhanced functionality would be predicated 271 on the synergistic action of its core components. Firstly, KICKs integrated into the line segments 272 would act as programmable switches controlling the signal flow. Secondly, the directionality of 273 soliton propagation would be rigorously enforced by integrated soliton diodes. Thus, the diodes 274 and switches placed in the all-JJTL lines determine the direction of soliton propagation in the line. 275 Finally, to solve the problem of collisions in a dense matrix, we propose dedicated vertical lines 276 that enable row-skipping connections. For the same purposes, horizontal lines can also be used for 277 column-skipping connections. 278 At first glance, it may seem that the proposed architecture is a complicated version of a memris-279 tive crossbar, but this is not the case. The main distinction is in the organization of interconnections 280 between lines: in a memristive crossbar, as the name follows, these connections are formed by the 281 intersection of signal lines and the corresponding memristive layer. In the proposed WayMatrix, 282 however, the lines are combined into a single node at the intersection point, the current direction 283 of which can be controlled by switches and diodes. The power of the WayMatrix architecture lies in its potential use as a universal framework for creating programmable and reconfigurable con-285 nections between different circuit blocks. WayMatrix makes it easy to set up feedback loops be-286 tween these blocks, change their connection order, and perform logical operations. We envision it 287 serving as a reconfigurable "backbone" to link various specialized functional units within a larger integrated circuit. For example, the WayMatrix could be configured to connect arrays of memory 289 cells to arithmetic logic units or to route data between different processing cores. Another key application is the creation of programmable clock distribution networks. In such a role, the WayMa-291 trix could manage signal timing across a chip by introducing precise, configurable delays into the

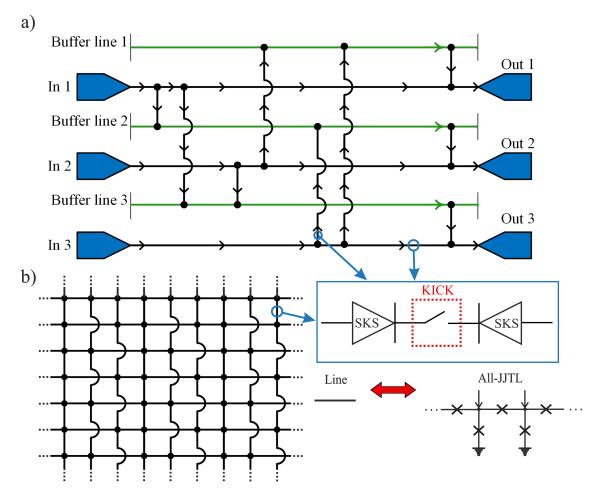


Figure 4: a) Schematic of an all-Josephson-junction transmission line (all-JJTL) network with three inputs (In 1, In 2, In 3), three outputs (Out 1, Out 2, Out 3), and three auxiliary buffer lines. Black arrows on the lines indicate soliton propagation paths. Input-output connections are configured by setting operation modes of kinetic inductance controllable keys (KICKs), where each cell either transmits or blocks solitons based on its programmed state. b) The schematic shows a transmission line matrix where path selection is governed by KICKs and signal directionality is ensured by soliton diodes. Specific vertical lines enable row-skipping connections to prevent soliton collisions during signal propagation.

clock paths, which is crucial for asynchronous circuit design. This would allow a single hardware 293 platform to be flexibly repurposed for different algorithms by simply re-programming the routing paths, a paradigm central to the development of superconducting programmable gate arrays (SP-295 GAs). 296 The true potential of this architecture, however, is most evident in its application as an axon-297 synaptic connection matrix for neuromorphic computing. The ability to program connections, en-298 force directionality, and reconfigure paths makes the WayMatrix an ideal candidate for emulating 299 the complex and plastic connectivity of a biological neural network. In such a system, each soliton 300 acts as a "spike", and the WayMatrix serves as the synaptic network that routes these spikes be-301 tween artificial neurons. This lays the groundwork for building powerful, event-driven and energy-302 efficient spiking neural networks based on the principles we have outlined. In addition to using the 303 WayMatrix, we can reconfigure the neural network itself, program connections between different 304 neurons, implement synaptic pruning, and even "kill" part of the artificial brain. 305 Human or animal brain contains a huge number of synapses, many times greater than the num-306 ber of neurons (e.g. the Norwegian rat brain contains about 200 million neurons, each of which 307 roughly has an average of about 1000 synapses [46]). The ability of a living being to solve certain 308 tasks depends precisely on the number of inter-neuronal connections. In their attempts to imple-309 ment such complex systems in hardware, engineers and scientists inevitably face the problem of 310 interconnects and the implementation of a huge number of synaptic connections. The supercon-311 ducting axon-synaptic matrix based on the WayMatrix concept seems to be a promising solution to 312 the problem [47-50]. 313 As mentioned above repeatedly, the field applications of kinetic inductance and, in particular, 314 KICK, are also extended to bio-inspired neuromorphic spiking networks. One important feature of living nervous tissues is the ability to modulate the synaptic delay of signal propagation from 316 one neuron to another. This feature is equally important to implement in hardware artificial realisations of neuromorphic networks. The signal propagation delay is also affected by a length and 318 a conductivity of an axon, which is quite simply imitated by means of a standard Josephson transmission line, as well as by means of all-JJTL, discussed at the beginning of this article. A simple solution to modulate the propagation delay is to change the length (number of JTL cells) of such an artificial axon, but there is another way. The inductance connected in parallel with the Josephson junctions determines the amount of magnetic energy stored within each JTL cell. Consequently, a larger inductance value results in a longer propagation delay.

Conclusions

This study demonstrates the programmable control of kinetic soliton dynamics in all-Josephson-326 junction networks through a novel tunable element, the Kinetic Inductance Controllable Key 327 (KICK). By engineering inhomogeneity via controlled kinetic inductance, we induce distinct dy-328 namical modes (Open, Close, T-Mode) that fundamentally alter soliton propagation. Furthermore, 329 the features of the proposed cell enable a soliton diode effect, achieving non-reciprocal signal trans-330 mission. Building on these principles, we propose two scalable architectures: a programmable 331 switch for reconfigurable routing and the WayMatrix, a versatile N * M routing matrix. These so-332 lutions establish a framework for robust, high-speed superconducting logic that addresses critical 333 bottlenecks in this type of computing. 334 We realize that the time required to "reprogram" kinetic inductance significantly exceeds the pi-335 cosecond timescales of Josephson junction dynamics. However, this re-configuration time should 336 be considered in the context of hardware development cycles. From this point of view, the re-337 configuration time is orders of magnitude lower than the time required to design, fabricate, and test 338 a new application-specific integrated circuit (ASIC), offering a compelling advantage in flexibility 339 and prototyping rate. 340 The superconducting diodes proposed in this work can be used as a part of synaptic connections 341 in neuromorphic networks to prevent the backward influence of a postsynaptic neuron on a presy-342 naptic neuron through the same connection link. It should also be noted that the signal propagation 343 time between neurons can be controlled by modulating the bias currents, the value of which di-344 rectly affects the potential barrier in Josephson line (standard JTL or All-JJTL). Thus, the choice of 345

a particular method of signal propagation delay influence depends on the realization of interneuron 346 interactions and the need to adjust a particular interneuron connection. Moreover, these approaches can be combined into one by using a chain of superconductor diodes. Using cells with kinetic in-348 ductances, we can change the local propagation speed of spikes in inter-neuronal signal transmis-349 sion circuits by smoothly adjusting the delay time. The integration of the WayMatrix will make it 350 possible to change the length of the axonal line as a whole, and thus introduce a delay. Besides, it 351 is really interesting to examine how the dynamics of voltage spike formation in a bio-inspired neu-352 ron, proposed in [42], will change if we substitute geometric inductances for kinetic ones. Further 353 development of the idea presented in this article will also address this aspect. 354 The proposed technique allows for a more compact design and new (diode) functionality of various 355 superconducting computing modules, and makes possible further increase of integration density 356 compared to well-known RSFQ technology. 357

358 Funding

N.K. and M.K. are grateful to Russian Science Foundation for the support of theoretical study of controlled kinetic inductance as a programmable element for superconducting logic (Project No. 25-19-00057). S.B. and I.S. are grateful the Ministry of Science and Higher Education of the Russian Federation for the support of the study of reconfigurable networks. (Agreement No. 075-15-2025-010). A.M. is grateful to the Foundation for the Advancement of Theoretical Physics and Mathematics "BASIS" (A.M. grant 24-2-10-6-1).

References

- 1. Likharev, K. K. *Dynamics of Josephson junctions and circuits*; Gordon and Breach science publishers, 1986.
- 2. Likharev, K. K. *Physica C: Superconductivity and its applications* **2012**, 482, 6–18.

- 369 3. Holmes, D. S.; Ripple, A. L.; Manheimer, M. A. *IEEE Transactions on Applied Superconductivity* **2013**, *23* (3), 1701610–1701610.
- 4. Tolpygo, S. K. *Low Temperature Physics* **2016**, *42* (5), 361–379.
- 5. Ahmad, M.; Giagkoulovits, C.; Danilin, S.; Weides, M.; Heidari, H. *Advanced Intelligent Systems* **2022**, *4* (9), 2200079.
- 6. Markovic, D.; Mizrahi, A.; Querlioz, D.; Grollier, J. *Nature Reviews Physics* **2020**, *2* (9), 499–510.
- 7. Schneider, M.; Toomey, E.; Rowlands, G.; Shainline, J.; Tschirhart, P.; Segall, K. *Superconductor Science and Technology* **2022**, *35* (5), 053001.
- 8. Jardine, M. A.; Fourie, C. J. *IEEE Transactions on Applied Superconductivity* **2023**, *33* (4), 1–9.
- 9. Karamuftuoglu, M. A.; Bozbey, A.; Razmkhah, S. *IEEE Transactions on Applied Superconductivity* **2023**, *33* (8), 1–7.
- ³⁸² 10. Yamanashi, Y.; Nakaishi, S.; Sugiyama, A.; Takeuchi, N.; Yoshikawa, N. *Superconductor Science and Technology* **2018**, *31* (10), 105003.
- Soloviev, I.; Ruzhickiy, V.; Bakurskiy, S.; Klenov, N.; Kupriyanov, M. Y.; Golubov, A.;
 Skryabina, O.; Stolyarov, V. *Physical Review Applied* **2021**, *16* (1), 014052.
- Maksimovskaya, A. A.; Ruzhickiy, V.; Klenov, N. V.; Bakurskiy, S. V.; Kupriyanov, M. Y.;
 Soloviev, I. I. *JETP Letters* **2022**, *115* (12), 735–741.
- Salameh, I.; Friedman, E. G.; Kvatinsky, S. *IEEE Transactions on Circuits and Systems II:* Express Briefs 2022, 69 (5), 2533–2537.
- Tanemura, S.; Takeshita, Y.; Li, F.; Nakayama, T.; Tanaka, M.; Fujimaki, A. *IEEE Transactions on Applied Superconductivity* **2023**, *33* (5), 1–5.

- 15. Jabbari, T.; Bocko, M.; Friedman, E. G. *IEEE Transactions on Applied Superconductivity*2023, *33* (5), 1–7.
- 16. Razmkhah, S.; Pedram, M. Engineering Research Express 2024, 6, 015307.
- Annunziata, A. J.; Santavicca, D. F.; Frunzio, L.; Catelani, G.; Rooks, M. J.; Frydman, A.;
 Prober, D. E. *Nanotechnology* 2010, 21 (44), 445202.
- ³⁹⁷ 18. Adamyan, A.; Kubatkin, S.; Danilov, A. *Applied Physics Letters* **2016**, *108* (17), 172601.
- ³⁹⁸ 19. Mahashabde, S.; Otto, E.; Montemurro, D.; de Graaf, S.; Kubatkin, S.; Danilov, A. *Physical*³⁹⁹ *Review Applied* **2020**, *14* (4), 044040.
- 20. Splitthoff, L. J.; Bargerbos, A.; Grünhaupt, L.; Pita-Vidal, M.; Wesdorp, J. J.; Liu, Y.; Kou, A.;
 Andersen, C. K.; Van Heck, B. *Physical Review Applied* **2022**, *18* (2), 024074.
- Wang, C.-G.; Yue, W.-C.; Tu, X.; Chi, T.; Guo, T.; Lyu, Y.-Y.; Dong, S.; Cao, C.; Zhang, L.;
 Jia, X. et al. *Chinese Physics B* **2024**, *33* (5), 058402.
- ⁴⁰⁴ 22. Li, J.; Barry, P.; Cecil, T.; Lisovenko, M.; Yefremenko, V.; Wang, G.; Kruhlov, S.; Kara⁴⁰⁵ petrov, G.; Chang, C. *Physical review applied* **2024**, 22 (1), 014080.
- Ustavschikov, S.; Levichev, M. Y.; Pashenkin, I. Y.; Klushin, A.; Vodolazov, D. Y. Superconductor Science and Technology **2020**, 34 (1), 015004.
- Levichev, M. Y.; Pashenkin, I. Y.; Gusev, N.; Vodolazov, D. Y. *Physical Review B* **2023**, *108* (9), 094517.
- 25. Neilo, A.; Bakurskiy, S.; Klenov, N.; Soloviev, I.; Kupriyanov, M. *Nanomaterials* **2024**, *14* (3), 245.
- ⁴¹² 26. Neilo, A.; Bakurskiy, S.; Klenov, N.; Soloviev, I.; Kupriyanov, M. Y. *JETP Letters* **2025**, *121* (1), 58–66.

- ⁴¹⁴ 27. Fourie, C.; van Heerden, H. *IEEE Transactions on Applied Superconductivity* **2007**, *17* (2), 538–541.
- Hironaka, Y.; Hosoya, T.; Yamanashi, Y.; Yoshikawa, N. *IEEE Transactions on Applied Super- conductivity* **2022**, *32* (8), 1–5.
- 418 29. Hosoya, T.; Yamanashi, Y.; Yoshikawa, N. *IEEE Transactions on Applied Superconductivity* 419 2021, 31 (3), 1–6.
- 30. Katam, N. K.; Mukhanov, O. A.; Pedram, M. *IEEE Transactions on Applied Superconductivity* **2018**, 28 (2), 1–12.
- 31. Schuman, C. D.; Kulkarni, S. R.; Parsa, M.; Mitchell, J. P.; Date, P.; Kay, B. *Nature Computa- tional Science* **2022**, *2* (1), 10–19.
- 32. Kudithipudi, D.; Schuman, C.; Vineyard, C. M.; Pandit, T.; Merkel, C.; Kubendran, R.; Aimone, J. B.; Orchard, G.; Mayr, C.; Benosman, R. et al. *Nature* **2025**, *637* (8047), 801–812.
- 33. Schegolev, A. E.; Bastrakova, M. V.; Sergeev, M. A.; Maksimovskaya, A. A.; Klenov, N. V.;
 Soloviev, I. *Mesoscience & Nanotechnology* 2024, *I* (1), 01–01005. doi:10.64214/jmsn.01.
 01005.
- 34. Schegolev, A. E.; Klenov, N. V.; Bakurskiy, S. V.; Soloviev, I. I.; Kupriyanov, M. Y.;
 Tereshonok, M. V.; Sidorenko, A. S. *Beilstein Journal of Nanotechnology* 2022, *13*, 444–454.
 doi:10.3762/bjnano.13.37.
- 35. Merolla, P. A.; Arthur, J. V.; Alvarez-Icaza, R.; Cassidy, A. S.; Sawada, J.; Akopyan, F.; Jackson, B. L.; Imam, N.; Guo, C.; Nakamura, Y. et al. *Science* 2014, 345 (6197), 668–673.
- 36. Indiveri, G.; Liu, S.-C. *Proceedings of the IEEE* **2015**, *103* (8), 1379–1397.
- 37. Crotty, P.; Schult, D.; Segall, K. *Physical Review E* **2010**, 82 (1), 011914.

- 38. Goteti, U. S.; Dynes, R. C. *Journal of Applied Physics* **2021**, *129* (7), 073901. doi:10.1063/5.
- 39. Semenov, V. K.; Golden, E. B.; Tolpygo, S. K. *IEEE Transactions on Applied Superconductiv- ity* **2023**, *33* (5), 1–8.
- 40. Feldhoff, F.; Toepfer, H. *IEEE Transactions on Applied Superconductivity* **2024**, *34* (3), 1–5. doi:10.1109/tasc.2024.3355876.
- 41. Skryabina, O. V.; Schegolev, A. E.; Klenov, N. V.; Bakurskiy, S. V.; Shishkin, A. G.; Sotnichuk, S. V.; Napolskii, K. S.; Nazhestkin, I. A.; Soloviev, I. I.; Kupriyanov, M. Y. et al.

 Nanomaterials **2022**, *12* (10), 1671.
- 42. Schegolev, A. E.; Klenov, N. V.; Gubochkin, G. I.; Kupriyanov, M. Y.; Soloviev, I. I. *Nanoma-terials* 2023, *13* (14), 2101.
- 43. Maksimovskaya, A. A.; Ruzhickiy, V. I.; Klenov, N. V.; Schegolev, A. E.; Bakurskiy, S. V.; Soloviev, I. I.; Yakovlev, D. S. *Chaos, Solitons & Fractals* **2025**, *193*, 116074.
- 44. Dormand, J.; Prince, P. *Journal of Computational and Applied Mathematics* **1980**, *6* (1), 19–26.
- 45. Shampine, L. F.; Reichelt, M. W. SIAM Journal on Scientific Computing 1997, 18 (1), 1–22.
- 46. Swanson, L. W. *Journal of Comparative Neurology* **2018**, *526* (6), 935–943.
- 453 47. Xia, Q.; Yang, J. J. Nature materials **2019**, 18 (4), 309–323.
- 48. El Mesoudy, A.; Lamri, G.; Dawant, R.; Arias-Zapata, J.; Gliech, P.; Beilliard, Y.; Ecoffey, S.;
 Ruediger, A.; Alibart, F.; Drouin, D. *Microelectronic Engineering* **2022**, *255*, 111706.
- 49. Chakraborty, I.; Ali, M.; Ankit, A.; Jain, S.; Roy, S.; Sridharan, S.; Agrawal, A.; Raghunathan, A.; Roy, K. *Proceedings of the IEEE* **2020**, *108* (12), 2276–2310.

50. Xu, Q.; Wang, J.; Yuan, B.; Sun, Q.; Chen, S.; Yu, B.; Kang, Y.; Wu, F. *IEEE Transactions on Automation Science and Engineering* **2021**, *20* (1), 74–87.